

## HARMONIC INTELLIGENT CONTROL WITH ACTIVE POWER FILTER

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### ABSTRACT

*This paper presents the procedures required to determine the locations and sizes of multiple-Active Power Filters (APFs) to reduce harmonic distortion in a power system with harmonic sources. The objective function is to minimize the APFs injection currents while satisfying harmonic voltages, total harmonic voltage distortions within IEEE-519 recommended limits and the maximum rating current limit of each installed APF. The APFs are located at the points having harmonic sources or higher harmonic distortions, while minimizing injection currents. The ANN Model is developed to determine size and location of APF. A typical 28-bus distribution system is selected to verify the validity of the proposed procedures.*

### 1- INTRODUCTION

Harmonic filters are recommended if a problem exists with harmonic distortion before the application of power factor correction, or if the harmonic distortions above the limits. Passive filters (PFs) have traditionally been used to absorb harmonic currents generated by non-linear loads, because of their low cost and high efficiency. The installation of tuned filters does however have some major, well documented, disadvantages that impair their effectiveness[1,6]. **First:** (PFs) may cause parallel resonance with the AC source. Therefore, amplification of the harmonic currents of the source side at specific frequencies occurs. **Second:** The passive filter can generate a series resonance with the AC source.

The problems associated with passive filters has led to the development of the parallel connected active filter. The active filter comprises a reactive energy storage element on the compensator DC side, a high frequency power electronic converter and an output filter that is used for filtering of the converter switching frequency on the output current. The characteristics of the active filters are [2,8] : **First:** The performance obtained with APFs is high compared with that obtained with PFs. **Second:** The initial and running costs of the APFs are high compared to that of the passive filters. The APFs are located at the points having harmonic sources or higher harmonic distortions, while minimizing injection currents are determined using a nonlinear optimization technique based ANN algorithm[3]. The ANN Model is developed to determine size and location of APF[4].

### 2- PROBLEM FORMULATION

Consider an N-bus power system that contains nonlinear loads (sources of harmonics). Also, there are M-buses which candidates for APFs placement to reduce harmonic distortion[5,7].

#### 2.1 Objective Function

The high rating of power electronic converter used in APFs causes high electro-magnetic interference, high power losses, and high cost of these filters. Due to these reasons, the objective function considered here is to minimize the nonsinusoidal injecting current from the APFs located at buses (m=1, 2, ..., M).

The injection nonsinusoidal current of an APF at candidate bus m is given by,

$$I_m = \left[ \sum_{h=2}^H \{ (I_m^{h,r})^2 + (I_m^{h,i})^2 \} \right]^{0.5} \quad (1)$$

The objective function is to minimize the total of squared injection nonsinusoidal currents of APFs for a set of candidate buses M, then

$$f(I_m) = \sum_{m=1}^M \sum_{h=2}^H \{ (I_m^{h,r})^2 + (I_m^{h,i})^2 \} \quad (2)$$

where

H : Maximum harmonic order of interest.

M : Total number of candidate buses for installing APFs.

$I_m$  : Installed APF rating current at candidate bus m.

$I_m^{h,r}$  : Real part of APF current for harmonic h at candidate

bus m.  $I_m^{h,i}$  : Imaginary part of APF current for harmonic

h at candidate bus m.

#### 2.2 System Constraints

The system constraints considered here are

##### 2.2.1 Maximum APF rating current

The injected APF current must be upper constrained to its rating value. This constraint can be stated as,

$$I_m \leq I_m^* \quad (3)$$

where,  $I_m$  is given by equation (1), and  $I_m^*$  is the maximum rating current of an APF at installed bus m.

##### 2.2.2 Individual harmonic distortion voltages

There are two types of voltage harmonic distortion constraints in IEEE-519. The first type applies to individual harmonic voltages at bus k,

$$\frac{|V_k^{(h)}|}{|V_k^{(1)}|} \leq V_k^{(h)*} \tag{4}$$

for k= 1, 2, ..., N, and h= 2, 3, ..., H

where

$|V_k^{(h)}|$ : Voltage magnitude at bus k for harmonic h.

$|V_k^{(1)}|$ : Voltage magnitude at bus k at fundamental frequency.

$V_k^{(h)*}$ : Is the high limit of voltage distortion for harmonic h at bus k.

**2.2.3 Total harmonic distortion**

The second type of IEEE-519 constraints applies to total harmonic voltage distortion. The total harmonic voltage distortion at any bus k (THDk) is given by

$$THD_k = \frac{\sqrt{\sum_{h=2}^H |V_k^{(h)}|^2}}{|V_k^{(1)}|} \tag{5}$$

This constraint can be stated as,

$$THD_K \leq THD_K^* \tag{6}$$

Where,  $THD_K^*$  is the high limit of total harmonic distortion of voltage at bus k.

The harmonic voltage at bus k for harmonic h, after installing all M APFs, is

$$|V_K^{(h)}| = |V_{K,old}^{(h)} + \Delta V_K^{(h)}| \tag{7}$$

The voltage change at bus k is

$$\Delta V_K^{(h)} = \sum_{m=1}^M Z_{K,m}^{(h)} \cdot I_m^{(h)} \tag{8}$$

Where  $V_{K,old}^{(h)}$  is the voltage distortion at bus k before installing the APFs, and  $Z_{K,m}^{(h)}$  is the harmonic transfer impedance between bus k and m.

A general problem formulation for the determination of locations and sizes of APFs to reduce harmonic distortion is

$$\text{Min } f(X) \tag{9}$$

Subject to

$$h(X) \leq 0 \tag{10}$$

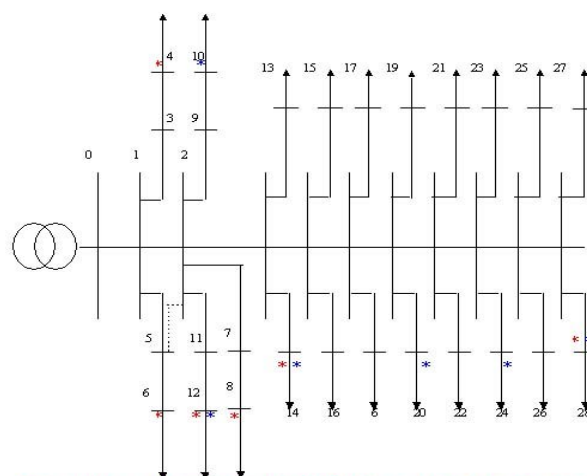
$$g(X) \leq 0 \tag{11}$$

Where f represents the sum of squared injection currents of APFs for a set of candidate buses (equation (2)), the vector of h signifies the inequality constraints for the maximum installed amount of APF for each candidate bus (equation (3)); the vector g signifies the inequality constraints for the harmonic IEEE-519 standard (equations (6, 10)). The vector of x includes the real and imaginary components of the injection currents for each APF at each harmonic order. We can see from the above formulation that the objective function is quadratic and

the constraints are also quadratic and convex.

**3 - SOLUTION PROCEDURE USING NEURAL NETWORK**

<b>Step1</b>	: Build harmonic data base and determine the spectrum of harmonic voltage and current at each bus.
<b>Step2</b>	: Identify the buses that have higher harmonic distortion.
<b>Step3</b>	: Install APF at buses of A - increased harmonics or B - sources of harmonics.
<b>Step4</b>	: Inject harmonic current at APF buses by a percentage 25% of injected harmonic current.
<b>Step5</b>	: Repeat steps from 1 to 4 for 50%, 75% and 100% of injected harmonic current.
<b>Step6</b>	: Identify for each APF busbar the value of THD and minimum value of injected current.
<b>Step7</b>	: Repeat steps for all busbars with increased and sources of harmonics.
<b>Step8</b>	: Build harmonic ANN patterns.
<b>Step9</b>	: Train the ANN with (THD) before and Harmonic injection current ( $I_5, I_7, I_{11}$ and $I_{13}$ ) at each APF busbar and output is busbars THD after installation.
<b>Step10</b>	: Test the trained ANN of APF with selected patterns.
<b>Step11</b>	: Identify the sizing of APF.



**Fig (1) One-line diagram for 28-bus distribution system.**

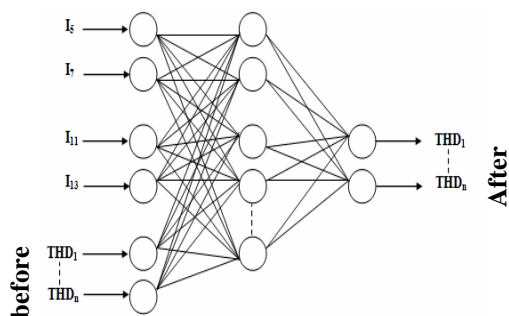


Fig (2) Neural Network Architecture

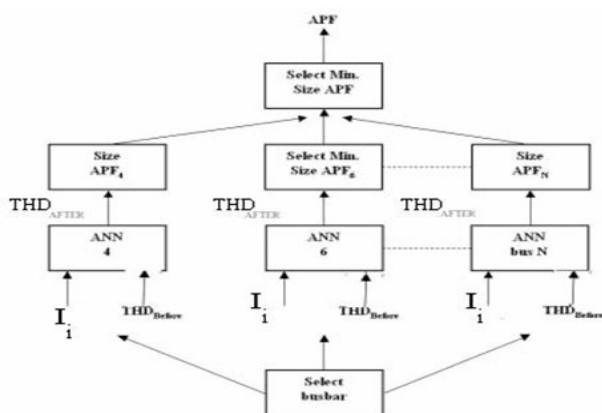


Fig (3) Flow chart of the ANN Based Technique

4 – TEST RESULTS

The proposed solution procedures for multiple APFs problem are tested using a typical 28-bus industrial distribution system. The single line diagram for the test system are given in Figure 1. The system has 13 main busbars with different loads. There are nonlinear loads at buses 4, 6, 8, 12, 14 and 28 which inject harmonic currents into the system. The dominant harmonics in the system are of the order 5, 7, 11 and 13. Before installing the APFs, the harmonic voltages, total harmonic distortions are determined by the harmonic power flow program. The buses 10, 12, 14, 24 and 28 having the higher THDs, it is reasonable to install the APFs at buses of - increased harmonics or sources of harmonics. by the proposed method. The solution procedure using Neural Network is then utilized to determine the optimal injections from multiple APFs, located at these buses to meet IEEE-519 standard limits and Identify the sizing of APF. By injecting harmonic current at APF busbar by 25%, 50%, 75% and 100% of injected harmonic current, to identify the minimum value of THD and injected current. From results we find that the buses having higher harmonics distortion are the best locations to install the APFs for economical and Technical consideration from the result we find also that 50% of injected harmonic current, give the minimum value of THD and injected current. The individual harmonic voltages in P.U at buses 10, 12, 14, 24 and 28 before/after the installation of the

APFs are illustrated in figures 4-8 respectively. The harmonic voltages are greatly reduced by APFs injection currents at level 50% of injected harmonic current. Figure 9 shows Comparison between THDs (%) of voltage before/after the installation of the APF. The results show that THD is greatly decreased by using APF. Figure 10 shows the APF sizing at location of increased harmonics and location of harmonic sources. The results show also that the size of APF at increased harmonics at 50% is less than corresponding value at sources of harmonic

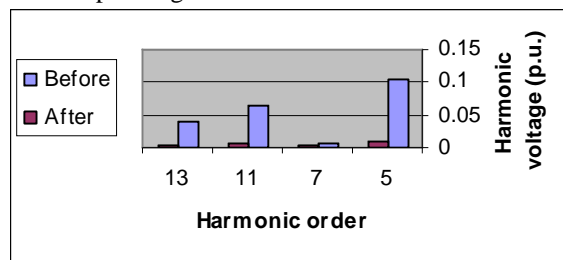


Fig (4) Harmonic voltages at bus 10 before/after APF installed.

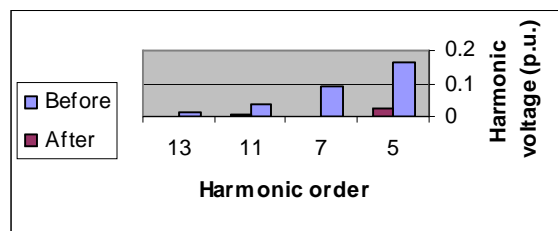


Fig (5) Harmonic voltages at bus 12 before/after APF installed.

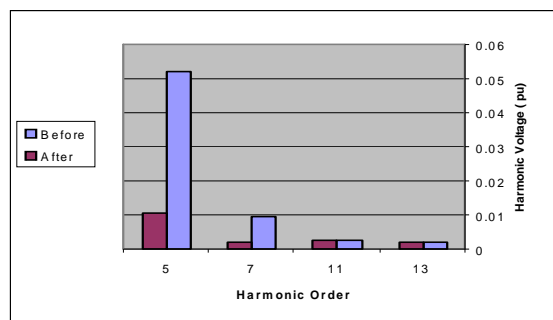


Fig (6) Harmonic voltages at bus 14 before/after APF installed.

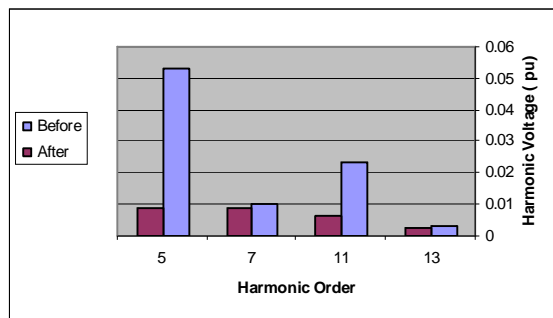
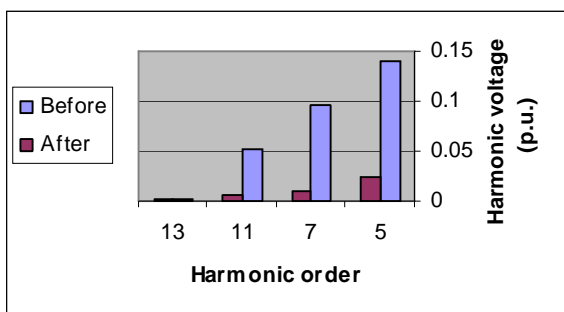
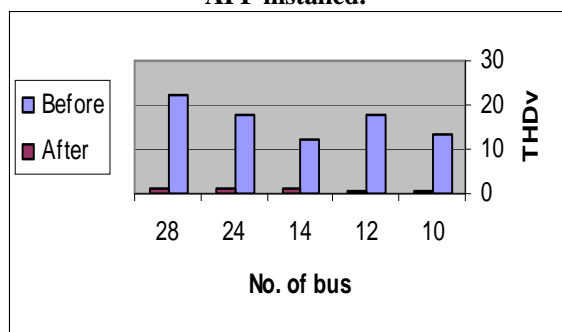


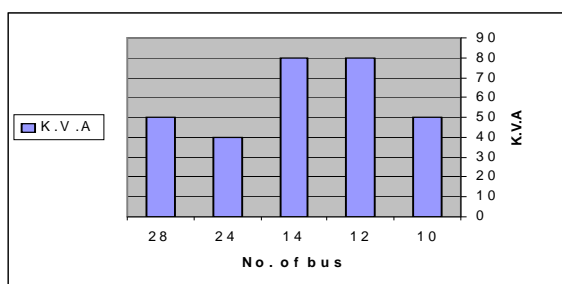
Fig (7) Harmonic voltages at bus 24 before/after APF installed.



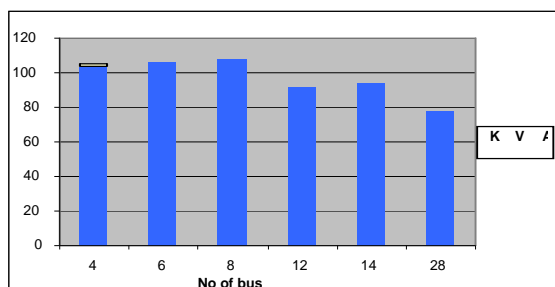
**Fig (8) Harmonic voltages at bus 28 before/after APF installed.**



**Fig (9) Comparison between THD (%) of voltage before/after the installation of the APF**



**Fig (10[A])**



**Fig (10[B])**

**Fig (10) ([A], [B]) The APF Sizing at location of increased harmonics and location of harmonic sources**

**5- CONCLUSION**

This paper presents the solution procedures required for applying multiple-APFs to reduce total harmonic distortion in a distribution system with harmonic sources.

The objective function is to minimize the total injection currents of APFs while satisfying the maximum limits of each APF rating current and the IEEE-519 recommended limits of harmonic voltages and total harmonic distortions. The points having the higher harmonic distortions are the best locations to install the APF. The minimum injection currents of APF are determined using the solution procedure of Neural Network. The test results for a typical 28-bus distribution system show that Neural Network is reliable and efficient for the determination of locations and sizes of APFs. The harmonic voltages are greatly reduced by APFs injection currents at level 50% of injected harmonic current. The results show that THD is greatly decreased by using APF. The results show also that the size of APF at increased harmonics at 50% is less than corresponding value at sources of harmonics.

**6- REFERENCES**

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