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COMPARISON OF CASCADED H-BRIDGE CONVERTERS AND MODULAR MULTILEVEL CONVERTERS FOR THE USE IN MEDIUM VOLTAGE GRID CONNECTED BATTERY ENERGY STORAGE SYSTEMS

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ABSTRACT

In this paper, the Cascaded H-Bridge (CHB) converter is compared to the Modular Multilevel Converter (M^2LC) regarding their efficiency and use of active and passive components. Design guidelines are developed.

INTRODUCTION

In recent years, the amount of energy decentrally "harvested" from renewable sources has been growing continously. In order to overcome problems in the transportation grid, Battery Energy Storage Systems (BESS) will play an increasing role in future energy systems.

Today, most storage systems use two- or three-level inverters. However, these are surpassed by modular multilevel topologies that provide benefits in terms of higher effective output frequency at a lower switching frequency, scalable design and error handling capability. The CHB (fig. 1) has already been proposed to be used as an inverter for BESS[5]. In this paper, it is compared to the M^2LC , a topology mostly used for HVDC purposes[4]. Variants enabling step-up operation are included into the examination. The inverter ratings used for the numerical evaluation are given in table 1. The inverter should be directly connected to the grid without an expensive and bulky transformer. The maximum peak output voltage and current are

$$\hat{v}_{o,max} = \sqrt{\frac{2}{3}} a_{ov} a_{vctrl} v_{LL,N} = 19.76 \text{ kV}$$
 (1)

$$\hat{i}_{\rm o,max} = \sqrt{\frac{2}{3}} \frac{P_{\rm inv}}{a_{\rm uv} v_{\rm LL,N}} = 480 \,\mathrm{A}$$
 (2)



Figure 1: CHB topology, its analytic simplified model and its battery modules

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Description	Symbol	Value
apparent power	$S_{\rm inv}$	10 MVA
grid line-to-line voltage	$v_{\rm LL,N}$	20 kV
switching frequency	$f_{\rm s}$	$750~{ m Hz}$
grid undervoltage[1]	$a_{\rm uv}$	0.85
grid overvoltage[1]	$a_{\rm ov}$	1.1
voltage reserve for controller	a_{vctrl}	1.1
DCDC voltage reserve	$a_{\rm bsm}$	0.98
voltage variation in modules	$a_{\Delta v cap}$	1.2
min. battery cell voltage[7]	$v_{\text{cell,min}}$	$1.75~\mathrm{V}$
nominal battery cell voltage[7]	$v_{\rm cell.N}$	$2 \mathrm{V}$
max. battery cell voltage[7]	$v_{\rm cell,max}$	$2.45~\mathrm{V}$

Table 1: Parameters used for dimensioning example

SIMPLIFIED MODELS

Both modular multilevel inverters consist of 3 or 6 branches that incorporate several identical cells functioning as switchable voltage sources. The cells with full-bridge outputs can supply $+v_{\rm cap}$, 0 and $-v_{\rm cap}$ at their terminals, the cells with half-bridge outputs $+v_{\rm cap}$ and 0.

The control pattern used to balance the energy among the modules and adjust the branch set-point voltage has been discussed before [3, 6, 8] and will not be regarded here. For the calculations, it is assumed that all modules operate at



Figure 2: M²LC topology, its simplified model and both types of modules considered

the same voltage and the same switching frequency of $f_s = 750$ Hz. With a sufficiently high number of modules per branch, the modules together with a series inductor can be described as continously adjustable voltage sources[4].

With this simplification, it is easily possible to determine currents and voltages in the converter branches. From this knowledge, the installed switching power, the energy variation per branch and the efficiency can be calculated.

INSTALLED SWITCHING POWER CHB without DCDC converter

Here, the sum of maximum module voltages per branch $v_{\rm br,max}$ depends on the inverter's total nominal battery voltage $v_{\rm bat,N}$ scaled to its 100% SOC value by $\frac{v_{\rm cell,max}}{v_{\rm cell,N}}$:

$$v_{\rm br,max}^{\rm CHB} = \frac{v_{\rm cell,max}}{v_{\rm cell,N}} \cdot \frac{v_{\rm bat,N}}{3} \qquad . \tag{3}$$

Thus, the sum of the switching power of all IGBTs P_{sw} is

$$P_{\rm sw}^{\rm CHB} = 3 \cdot 4 \cdot v_{\rm br,max}^{\rm CHB} \cdot \hat{i}_{\rm o,max}$$
(4)

and has a minimum of 159 MVA at the smallest possible battery voltage. So, the optimal $v_{\rm bat,N}$ considering $P_{\rm sw}$ is

$$v_{\rm bat,N,optsw}^{\rm CHB} = \frac{v_{\rm cell,N}}{v_{\rm cell,min}} \hat{v}_{\rm o} \qquad . \tag{5}$$

CHB with DCDC converter

Adding a DCDC step-up converter changes the variation in module voltage to $a_{\Delta v cap}$. Also, the battery voltage needs to be smaller than the module voltage at all times (factor $a_{\rm bsm} = 0.98$) for proper current decoupling. This gives two lower limits for the peak branch voltage $v_{\rm br,max}$:

$$v_{\rm br,max}^{\rm CHB,DCDC} \geq a_{\Delta v cap} \hat{v}_{\rm o,max}$$
 and (6)

$$v_{\rm br,max}^{\rm CHB,DCDC} \geq \frac{v_{\rm bat,N}}{3a_{\rm bsm}} \frac{v_{\rm cell,max}}{v_{\rm cell,N}} a_{\Delta v cap}$$
 . (7)

Assuming the same switches are used for DCDC converter and output stage, their maximum current $i_{sw,max}$ has lower limits from both:

$$i_{\rm sw,max}^{\rm CHB,DCDC} \geq \frac{P_{\rm inv}}{v_{\rm bat,N}} \frac{v_{\rm cell,N}}{v_{\rm cell,min}}$$
 (DCDC) and (8)

$$i_{\rm sw,max}^{\rm CHB,DCDC} \geq \hat{i}_{o,max}$$
 (output). (9)

The resulting switching power is a function of $v_{\rm bat,N}$ and has a (constant) minimum between

$$\underbrace{\frac{P_{\text{inv}}}{\hat{i}_{\text{o,max}}} \frac{v_{\text{cell,N}}}{v_{\text{cell,min}}}}_{23.8 \text{ kV}} \leq v_{\text{bat,N,optsw}}^{\text{CHB,DCDC}} \leq \underbrace{3 \underbrace{\frac{v_{\text{cell,N}}}{v_{\text{cell,max}}} a_{\text{bsm}} \hat{v}_{\text{o,max}}}_{47.4 \text{ kV}}$$

Below 23.8 kV, the installed switching power is determined by (6) and (8), above 47.4 kV by (7) and (9). The minimum is given by (6) and (9) evaluates to

$$P_{\rm sw,min}^{\rm CHB,DCDC} = 18 \cdot a_{\Delta \rm vcap} \cdot \hat{v}_{\rm o,max} \cdot \hat{i}_{\rm o,max} \approx 205 \; {\rm MVA}.$$

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M²LC with half bridge modules

Using half-bridge modules, the battery voltage must be:

$$v_{\text{bat,N}}^{\text{M}^{2}\text{LCHB}} \geq 2 \frac{v_{\text{cell,N}}}{v_{\text{cell,min}}} \hat{v}_{\text{o,max}}$$
 . (10)

Battery voltage and output voltage determine the maximum branch voltage:

$$v_{\rm br,max}^{\rm M^2LCHB} = \left(\frac{v_{\rm cell,max}}{2 \cdot v_{\rm cell,N}} v_{\rm bat,N}^{\rm M^2LC} + \hat{v}_{\rm o,max}\right) \cdot a_{\Delta \rm vcap} \quad (11)$$

The maximum branch current is equal for upper and lower branch. Assuming a constant charging current, it results in

$$i_{\rm sw,max}^{\rm M^2LCHB} = \frac{P_{\rm inv}}{3 \cdot v_{\rm bat,N}^{\rm M^2LC}} \frac{v_{\rm cell,N}}{v_{\rm cell,min}} + \frac{i_{\rm o,max}}{2} \quad (12)$$

The installed switching power has a minimum at the minimum possible battery voltage. It is

$$P_{\rm sw}^{\rm M^2LCHB} = 6 \cdot 2 \cdot i_{\rm sw,max}^{\rm M^2LC} \cdot v_{\rm br,max}^{\rm M^2LC} \approx 222 \text{ MVA}.$$

M²LC with full bridge modules

With full-bridge modules, the limit (10) doesn't apply any more because with negative branch voltages, the output voltage may be greater than half the DC-link voltage. (11) and (12) are still valid. The required switching power

$$P_{\rm sw}^{\rm M^2LC} = 6 \cdot 4 \cdot i_{\rm sw,max}^{\rm M^2LC} \cdot v_{\rm br,max}^{\rm M^2LC}$$
(13)

has a minimum at

$$v_{\text{bat,N,optsw}}^{\text{M}^{2}\text{LCFB}} = \frac{2\sqrt{a_{\text{uv}}a_{\text{ov}}a_{\text{vctrl}}}v_{\text{LL,N}}v_{\text{cell,N}}}{\sqrt{3v_{\text{cell,max}}}v_{\text{cell,min}}}$$
(14)

The minimum value is

$$\begin{split} P_{\rm sw,min}^{\rm M^2LCFB} &= 4a_{\Delta \rm vcap} P_{\rm inv} \left(\frac{v_{\rm cell,max}}{v_{\rm cell,min}} + \frac{2a_{\rm ov}a_{\rm vctrl}}{a_{\rm uv}} \right. \\ &+ \frac{2\sqrt{2v_{\rm cell,max}a_{\rm ov}a_{\rm vctrl}}}{\sqrt{v_{\rm cell,min}a_{\rm uv}}} \right) \approx 396 \; {\rm MVA}. \end{split}$$

MODULE CAPACITORS

The size of a branch's energy storage is determined by the energy fluctuation, i.e. the difference between the branch energy's maximum and minimum $\Delta e_{\rm br}$. In this section, the energy fluctuations $\Delta e_{\rm inv} = \sum \Delta e_{\rm br}$ occuring in the inverters' DC-links will be evaluated.

However, no attention is paid to smoothing the battery current as this topic is largly depending on battery properties such as surface capacitance, resistance and microcycle capability. It should be kept in mind, though, that CHB modules with or without DCDC converter normally should be equipped with a DC-link capacitor to supply high-frequency switching currents. In modules without DCDC converter, the capacitor may even be used to supply the double load frequency current.

CHB with DCDC converter

With the power transferred through a branch's DCDC converters being constant, the energy variation occuring in the inverter's module capacitors is

$$\Delta e_{\rm inv} = \frac{S_{\rm inv}}{\omega_{\rm o}} \approx 31.8 \,\rm kJ \qquad . \tag{15}$$

It is neither a function of the battery voltage nor of the power factor angle φ .

M^2LC

The dimensioning of the module capacitors for modulation indizes $m \leq 1$ has already been covered by [4]. However, with full-bridge modules, the modulation index is not limited to be $m \leq 1$ (or, with space vector modulation, to $m \leq \frac{2}{\sqrt{3}}$) any more. The derivation is omitted here; however, the results presented below are identical to those presented in [4] for $m \leq 1$. They apply to both full- and half-bridge versions. For compactness, the abbreviations $s_x = \sin(x)$, $c_x = \cos(x)$ and $m = \frac{2\hat{v}_o}{v_{\text{bat}}}$ are used. The energy variation depends on the point of operation:

The energy variation depends on the point of operation: For $m \leq 1$ and $0 \leq \varphi \leq \frac{\pi}{2}$ as well as $1 < m \leq \sqrt{2}$ and $0 \leq \varphi \leq \operatorname{atan}\left(\frac{2-m^2}{2\sqrt{m^2-1}}\right)$,

$$\Delta e_{\rm inv} = \frac{S_{\rm inv}}{\omega_{\rm o}} \frac{\left(4 - m^2 c_{\varphi}^2\right)^{\frac{3}{2}}}{2m} \qquad . \tag{16}$$

For $1 < m \le \sqrt{2}$ and $\operatorname{atan}\left(\frac{2-m^2}{2\sqrt{m^2-1}}\right) \le \varphi \le \frac{\pi}{2}$ as well as $\sqrt{2} \le m$ and $\operatorname{atan}\left(\frac{m^2-2}{2\sqrt{m^2-1}}\right) \le \varphi \le \frac{\pi}{2}$,

$$\Delta e_{\rm inv} = \frac{S_{\rm inv}}{4m^2\omega_{\rm o}} \left[m(4 - m^2 c_{\varphi}^2)^{\frac{3}{2}} + 4c_{\varphi}(m^2 - 1)^{\frac{3}{2}} + (4 + 4m^2 + m^4 c_{\varphi}^2)s_{\varphi} \right]$$
(17)

For $\sqrt{2} \le m$ and $0 \le \varphi \le \operatorname{atan}\left(\frac{m^2-2}{2\sqrt{m^2-1}}\right)$,

$$\Delta e_{\rm inv} = 2 \frac{S_{\rm inv}}{\omega_{\rm o}} \frac{c_{\varphi} \left(m^2 - 1\right)^{\frac{3}{2}}}{m^2} \qquad . \tag{18}$$



Figure 3: M²LC's energy variation over modulation index for different load angles

These results (fig. 3) have been verified by simulations of the simplified model.

EFFICIENCY

3.3 kV IGBTs have been chosen as switches for the inverter. They represent a compromise between higher switching power of 6.5 kV devices and good availability and low cost of low voltage switches. As the maximum currents differ between the inverters, assuming the same type of IGBT for all inverters would not be realistic. That is why the FZ1000R33HE, a 1000 A low-loss IGBT made by Infineon[2], has been chosen as reference and scaled to meet the inverters' current requirements. The forward voltages $v_{\rm f}$ of IGBT and diode are approximated as functions of current using splines.

The number of modules per branch $n_{\rm mpb}$ results from the switch's reference voltage $v_{\rm ref},$ see fig. 4:

$$n_{\rm mpb} = \left[\frac{v_{\rm br,max}}{v_{\rm ref}}\right]$$
 (19)

The average on-times of the single switches are calculated per branch as functions of time a(t). The conduction losses are then evaluated numerically for diode and IGBT of the single switches using

$$P_{\rm c} = \frac{\omega_{\rm o}}{2\pi} \int_0^{\frac{2\pi}{\omega_{\rm o}}} a(t)i(t)v_{\rm f}\left(i(t)\frac{i_{\rm ref}}{i_{\rm sw,max}}\right) {\rm d}t \qquad (20)$$

with i(t) being the current going through the respective device. The switching losses (IGBT turn-on, IGBT turnoff, diode reverse-recovery) are approximated similarly to the forward voltages according to the datasheet and then evaluated as

$$P_{\rm sw} = \frac{\omega_{\rm o} f_{\rm s}}{2\pi} \frac{v_{\rm br,max} i_{\rm sw,max}}{n_{\rm mpb} v_{\rm ref} i_{\rm ref}} \int_0^{\frac{2\pi}{\omega_{\rm o}}} e_{\rm sw} \left(i(t) \frac{i_{\rm ref}}{i_{\rm sw,max}} \right) {\rm d}t.$$

COMPARISON

Fig. 4 shows superior performance for the standard CHB inverter at first sight. It has the highest efficiency with real power and needs the smallest amount of installed switching power. However, it either exposes the batteries to low-frequency micro-cycles that may reduce their lifetime or it needs huge additional capacitances. Also, it needs the greatest battery voltage (and thus, the largest number of battery cells) for proper operation. This means higher battery managament cost.

Investing about a third of switching power and one inductor per module more by choosing a CHB with DCDC stage leaves only the DCDC converters' high-frequency ripples to the batteries. It allows to considerably reduce the number of battery cells; the number of required modules decreases by 6.Considering the batteries' efficiency, the inverter's increase of losses by half a percent may be acceptable.

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The M^2LC shows disadvantages in terms of component cost and efficiency. It also doesn't allow to disable a (damaged or empty) part of the battery the way the CHBs do. However, it is worth considering if the inverter is intended not only for connecting the battery but also some other kind of generator to the grid.

In terms of maximum efficiency at an assumed nominal cell voltage of $v_{\rm cell,N}=\frac{v_{\rm cell,max}+v_{\rm cell,min}}{2}$, the CHB with



Figure 4: Simulation results: Required switching power (top), worst-case energy variation (below), module count and efficiency (bottom) over the inverter's total nominal battery voltage. In the last plot, the load angle varies between 0° (low eff.) and 90° (high eff.).



Figure 5: Simulation results: Maximum efficiency at nominal battery voltage over battery voltage variation. Load angle varies between 0° (low eff.) and 90° (high eff.).

DCDC converters and the M^2LC with full-bridge modules show advantages for batteries with largly varying voltage (fig. 5). Generally, as long as the battery voltage is fixed, standard topologies are preferable.

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